P-Channel Enhancement Mode Field Effect Transistor

Product Summary

PRODUCT S	SUMMARY	
Vdss	ID	$R_{DS(ON)}(m \Omega)$ Typ
-30V	-5.3A	46@ VGS=-10V
	-3.3A	78 @ VGS=-4.5V

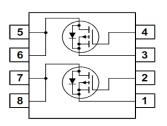
Features

- Supper high dense cell design for low RDS(ON)
- Rugged and reliable
- Simple drive requirement
- SOP-8 Package



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Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



Absolute Maximum Ratings(T_A = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	-30	V
Gate-Source Voltage	VGS	±20	V
Drain Current-Continuous ^a @Tj=125°C	Id	-5.3	А
- Pulse d^b	Ірм	-24	А
Drain-source Diode Forward Current ^a	Is	-1.7	А
Maximum Power Dissipation ^a	PD	2.5	W
Operating Junction and Storage Temperature Range	Tj,Tstg	-55 to 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to Ambient ^a	Rth JA	50	°C/W
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1

Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS				1		
Drain-Source Breakdown Voltage	BVDSS	VGS=0V,ID=-250µA	-30			V
Zero Gate Voltage Drain Current	IDSS	VDS=-24V,VGS=0V			-1	μA
Gate-Body Leakage	Igss	VGS=±20V,VDS=0V			±100	nA
ON CHARACTERITICS						
Gate Threshold Voltage	VGs(th)	VDS=VGS,ID=-250µA	-1	-1.5	-2.5	V
Drain-Source On-State Resistance	_	Vgs=-10V,ID=-5.3A		46	55	- mΩ
	RDS(ON)	Vgs=-4.5V,ID=-4.2A		78	83	
Forward Transconductance	gfs	VGS=-5V,ID=-5.3A		5		S
DAYNAMIC CHARACTERISTICS				1	1	
Input Capacitance	Ciss	VDS=-15V,VGS=0V f=1.0MHz		582		pF
Output Capacitance	Coss			125		pF
Reverse Transfer Capacitance	Crss	1-1:0M112		86		pF
SWITCHING CHARACTERISISTICS				1		
Turn-On Delay Time	td(ON)	V _{DD} =-15V I _D =-5.3A, V _{GEN} =-4.5V R _L =10ohm R _{GEN} =10ohm		9		ns
Rise Time	tr			10		ns
Turn-Off Delay Time	td(off)			38		ns
Fall Time	tf			23		ns
Total Gate Charge	Qg	VDS=-15V,ID=-1A		11.7		nC
Gate-Source Charge	Qgs			2.1		nC
Gate-Drain Charge	Qgd	V _{GS} =-10V		2.9		nC

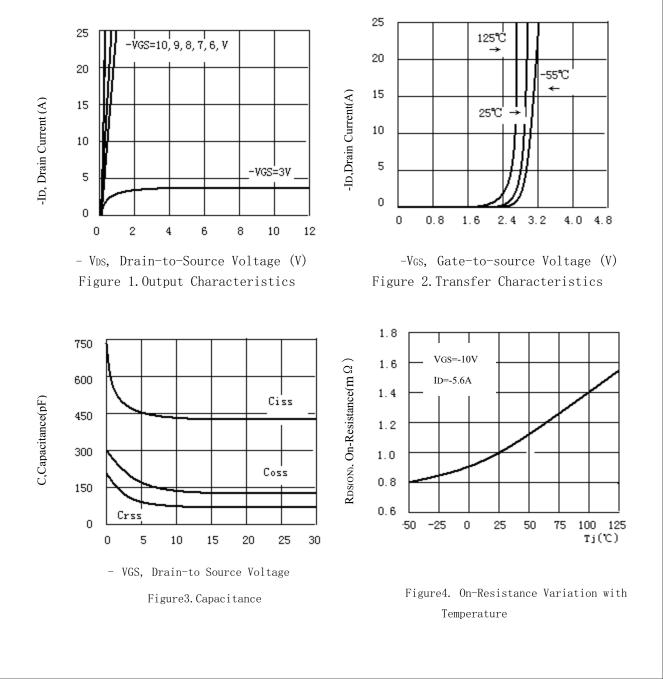
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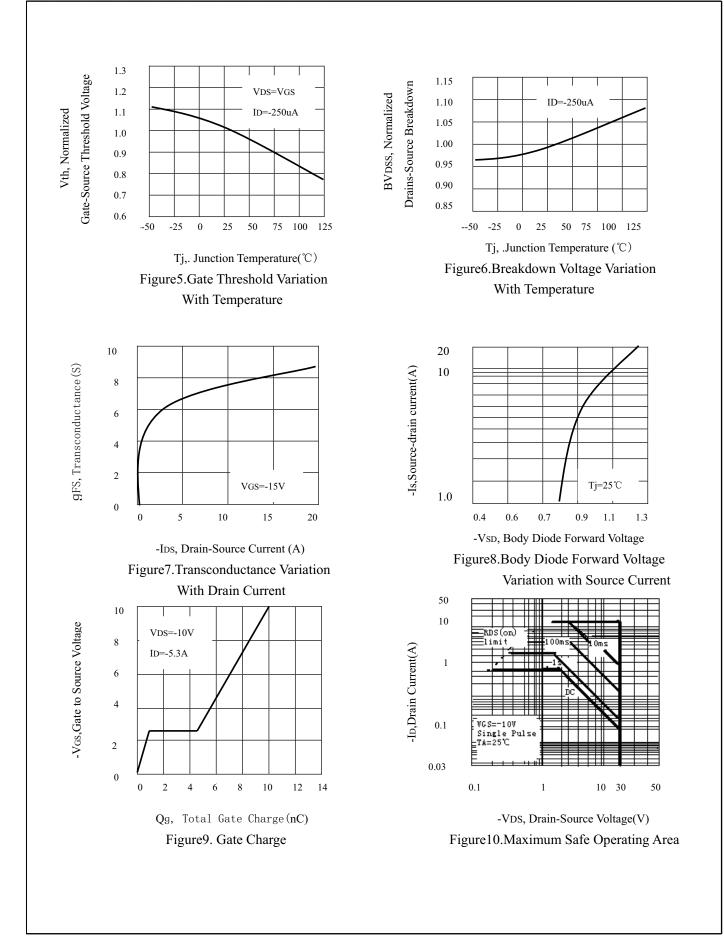
ELECTRICAL CHARACTERICS (TA=25°C unless otherwise noted)

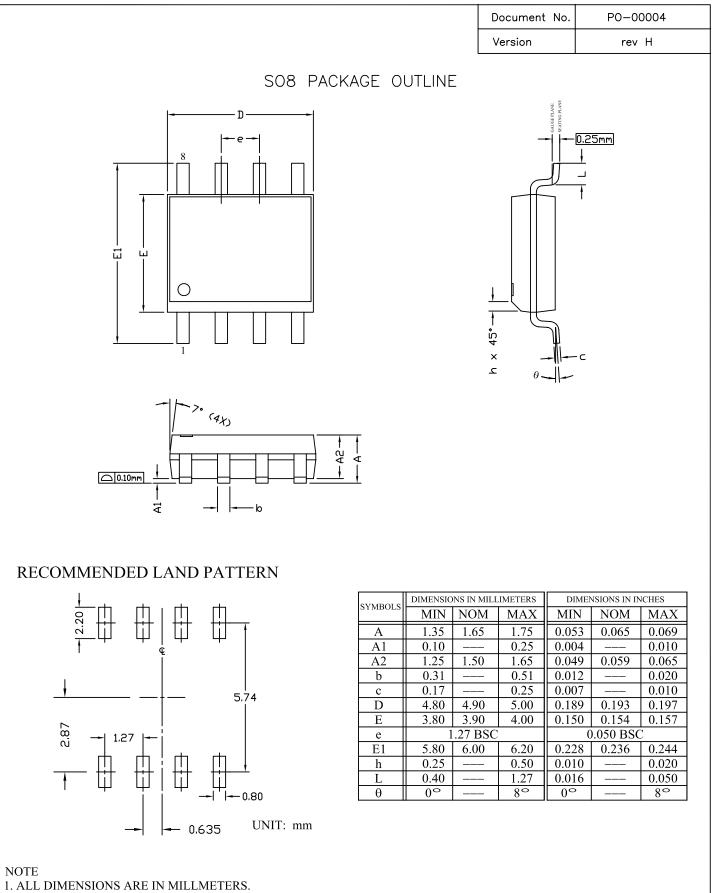
Parameter	Symbol	Condition	Min	Тур	Max	Unit
DRAIN-SOURCE DIODE CHARACT	ERISTICS					
Diode Forward Voltage	Vsd	Vgs=0V,Is=-1.7A		-0.84	-1.2	V

Notes

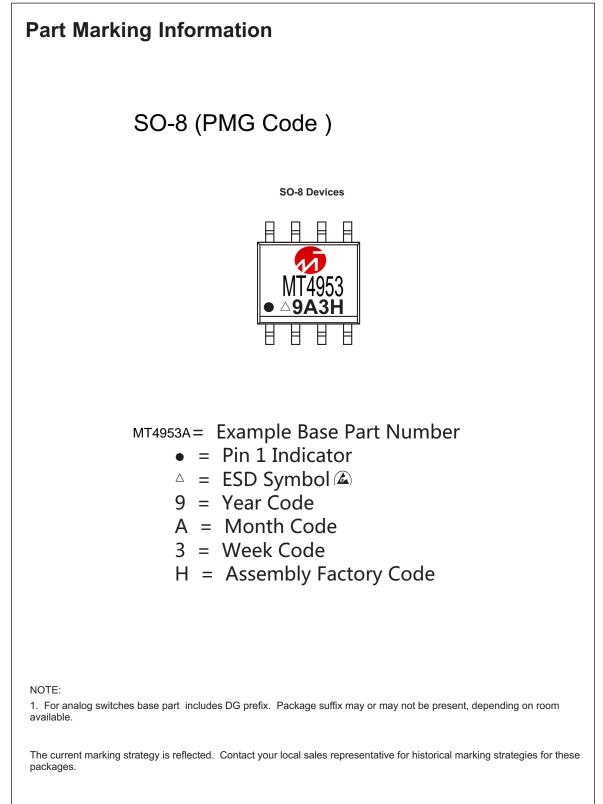
- a. Surface Mounted on FR4 Board, $t \leq 10$ sec
- b. Pulse Test: Pulse Width \leq 300Us, Duty Cycle \leq 2%
- c. Guaranteed by design, not subject to production testing.







- 2. DIMENSIONS ARE IN MILLINETERS.
- 3. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
- MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- 4. DIMENSION L IS MEASURED IN GAUGE PLANE.
- 5. CONTROLLING DIMENSION IS MILLIMETER.
- CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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